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ISSUE	
Class	Subclass

O.I.P.E.

PATENT DATE

SCANNED

Q.A.

APPLICATION NO. 09/872263	CONT/PRIOR	CLASS 712	SUBCLASS 11	ART UNIT 2183	EXAMINER J/C Carney
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TITLE	APPLICANTS
1. Chairman	1. Chairman
2. Vice Chairman	2. Vice Chairman
3. Secretary	3. Secretary
4. Treasurer	4. Treasurer
5. Member	5. Member
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99. Member	99. Member
100. Member	100. Member

Kenneth Lerman

System and method for the use of reset logic in high availability systems

PTO-2040
12/99

ISSUING CLASSIFICATION									
ORIGINAL				CROSS REFERENCE(S)					
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
INTERNATIONAL CLASSIFICATION									

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	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) _____ (Date)			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Primary Examiner) _____ (Date)			ISSUE FEE	
				Amount Due	Date Paid
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